Web Results 1 - 10 of about **342** for <u>loop</u> <u>bypass</u> <u>fault</u> <u>error</u> <u>failure</u> <u>restore</u> <u>diagnostic</u> <u>test</u> <u>subset</u>. (0.45 seconds)

[PDF] <u>On-line **testing** techniques for **fault** tolerance</u>
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... the block (branch free interval), a **loop**-free interval ... To **bypass** the control logic,
the conditions for control ... that already have some measure of **fault** tolerance ...
homepages.cae.wisc.edu/~ece753/papers/Paper_6.pdf - <u>Similar pages</u>

<u>Internet Draft David Allan(editor) Document: draft-allan-mpls-oam ...</u>
... unsignalled facility backup LSPs in "**bypass** tunnels" [SWALLOW ... detecting what would
genuinely constitute a routing **loop**. ... **diagnostic tests** (once a **fault** has been ...
ftp.ist.utl.pt/pub/drafts/ draft-allan-mpls-oam-frmwk-04.txt - 58k - <u>Cached</u> - <u>Similar pages</u>

> <u>Internet Draft David Allan, editor Document: draft-allan-mpls-oam ...</u>
> ... create unsignalled backup LSPs in "**bypass** tunnels" [SWALLOW ... what would genuinely
> constitute a routing **loop**. ... Mechanisms used for proactive **fault** detection gain ...
> ftp.ist.utl.pt/pub/drafts/ draft-allan-mpls-oam-frmwk-02.txt - 55k - <u>Cached</u> - <u>Similar pages</u>

<u>Internet Draft David Allan(editor) Document: draft-allan-mpls-oam ...</u>
... unsignalled facility backup LSPs in "**bypass** tunnels" [SWALLOW]. ... what would genuinely
constitute a routing **loop**. ... **Fault** management must be applicable across the ...
ftp.scarlet.be/pub/documentation/ internet-drafts/draft-allan-mpls-oam-frmwk-05.txt - 69k - <u>Cached</u> - <u>Similar pages</u>

<u>Computer-Related Abbreviations and Acronyms</u>
... Basic Teleprocessing Access Method - Extended Storage BYP **Bypass**. ... FSK Frequency Shift
Keying FT **Fault** Tolerant FTAM ... bit rate Digital Subscriber **Loop** HDTV High ...
www.guru3d.com/faq.php?cat=review&id=3 - 89k - <u>Cached</u> - <u>Similar pages</u>

[PDF] <u>System Self **Test** User's Guide</u>
File Format: PDF/Adobe Acrobat - <u>View as HTML</u>
... The memory **test bypass** option is not displayed when running ... **Test** (Version 2.0 and
later) 6.0**LOOP** SETTINGS The ... complete from disk drive 03 Write **fault** from disk ...
www.alphamicroproducts.com/dsm00/15602A00.pdf - <u>Similar pages</u>

<u>FW MX DW MX HTML</u>
... on a disk surface is a latent **fault** until an ... Port **bypass** circuits are typically found
in Fibre Channel ... **Loop** Device A Fibre Channel arbitrated **loop** device that ...
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<u>Sun Microsystems GmbH: Produkte - Hardware - Storage - Midrange ...</u>
... Automatic hardware-based **bypass** of the faulty drive in case of a faulty drive causing
loop disruptions ... FRUs get pulled for replacement; Summary **fault** LEDs on ...
de.sun.com/Produkte/Hardware/ Storage/midrange/6000/6100/6120/details2.html - 73k - <u>Cached</u> - <u>Similar pages</u>

<u>Glossary of Internet Terminology - Computer Acronyms</u>
... BYP **Bypass** ... Bus; FSF Free Software Foundation; FSK Frequency Shift Keying; FT **Fault**
Tolerant; ... HDSL High-bit rate Digital Subscriber **Loop**; HDTV High Definition TV; HE ...
www.sidetrips.com/support/glossary_acronyms.asp - 94k - <u>Cached</u> - <u>Similar pages</u>

[PDF] <u>SOFT COMPUTING TOOLS FOR TRANSIENT CLASSIFICATION 1 Introduction</u>
File Format: PDF/Adobe Acrobat - <u>View as HTML</u>
... operational (TTWBP), turbine trip without **bypass** (TTWOBP), main ... eg coming from a
fault detection system ... I&C systems (instrumentation, actuators or closed-**loop** ...
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(loop <or> ring) <and> diagnose <and> (error <or> fa **Search**

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1 **Active diagnosis of discrete event systems**
Sampath, M.; Lafortune, S.; Teneketzis, D.;
Decision and Control, 1997., Proceedings of the 36th IEEE Conference on
, Volume: 3 , 10-12 Dec. 1997
Pages:2976 - 2983 vol.3

[Abstract]   [PDF Full-Text (640 KB)]   **IEEE CNF**

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2 **Self-diagnosing intelligent motors: a key enabler for next generation manufacturing systems**
Discenzo, F.M.; Unsworth, P.J.; Loparo, K.A.; Marcy, H.O.;
Intelligent and Self-Validating Sensors (Ref. No. 1999/160), IEE Colloquium on
, 21 June 1999
Pages:3/1 - 3/4

[Abstract]   [PDF Full-Text (352 KB)]   **IEE CNF**

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3 **Active diagnosis of discrete-event systems**
Sampath, M.; Lafortune, S.; Teneketzis, D.;
Automatic Control, IEEE Transactions on , Volume: 43 , Issue: 7 , July 1998
Pages:908 - 929

[Abstract]   [PDF Full-Text (880 KB)]   **IEEE JNL**

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4 **A characterization of t/s-diagnosability and sequential t-diagnosability in designs**
Joo-Kang Lee; Butler, J.T.;
Computers, IEEE Transactions on , Volume: 39 , Issue: 10 , Oct. 1990
Pages:1298 - 1304

[Abstract]   [PDF Full-Text (680 KB)]   **IEEE JNL**

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5 **An intelligent sensor to monitor power system stability, performance and diagnose failures**
Doraiswami, R.; Jiang, J.;
Power Systems, IEEE Transactions on , Volume: 5 , Issue: 4 , Nov. 1990
Pages:1432 - 1438

[Abstract]   [PDF Full-Text (524 KB)]   **IEEE JNL**

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6 **Using neural networks to automatically refine expert system**

knowledge bases: experiments in the NYNEX MAX domain
Opitz, W.; Craven, M.W.; Shavlik, J.W.;
Neural Networks,1997., International Conference on , Volume: 1 , 9-12 June 1997
Pages:16 - 20 vol.1

[Abstract]   [PDF Full-Text (392 KB)]   **IEEE CNF**

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7 Diagnostics of eccentricities and bar/end-ring connector breakages in polyphase induction motors through a combination of time-series data mining and time-stepping coupled FE-state space techniques
Bangura, J.F.; Povinelli, R.J.; Demerdash, N.A.O.; Brown, R.H.;
Industry Applications Conference, 2001. Thirty-Sixth IAS Annual Meeting. Conference Record of the 2001 IEEE , Volume: 3 , 30 Sept.-4 Oct. 2001
Pages:1579 - 1586 vol.3

[Abstract]   [PDF Full-Text (640 KB)]   **IEEE CNF**

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8 Precision current monitor system for the power supplies at SRRC
Yuan-Chen Chien; Chen-Yao Liu; Chiou, J.; Kuo-Bin Liu; Keng Tzong Sheu;
Particle Accelerator Conference, 2001. PAC 2001. Proceedings of the 2001 , Volume: 5 , 18-22 June 2001
Pages:3690 - 3692 vol.5

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9 Automated test of ECUs in a hardware-in-the-loop simulation environment
Boot, R.; Richert, J.; Schutte, H.; Rukgauer, A.;
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Xu, J.;
Fault-Tolerant Computing, 1991. FTCS-21. Digest of Papers., Twenty-First International Symposium , 25-27 June 1991
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[Abstract]   [PDF Full-Text (696 KB)]   **IEEE CNF**

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11 System level diagnosis with local constraints
Das, A.; Thulasiraman, K.; Lakshmanan, K.B.; Agarwal, V.K.;
Circuits and Systems, 1989., IEEE International Symposium on , 8-11 May 1989
Pages:1891 - 1894 vol.3

[Abstract]   [PDF Full-Text (332 KB)]   **IEEE CNF**

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12 Detecting and diagnosing saturation faults
Noriega, J.R.; Hong Wang;
Control '96, UKACC International Conference on (Conf. Publ. No. 427) , Volume: 2 , 2-5 Sept. 1996
Pages:809 - 813 vol.2

[Abstract]   [PDF Full-Text (384 KB)]   **IEE CNF**

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13 Diagnostics of eccentricities and bar/end-ring connector breakages in polyphase induction motors through a combination of time-series data mining and time-stepping coupled FE-state-space techniques
Bangura, J.F.; Povinelli, R.J.; Demerdash, N.A.O.; Brown, R.H.;
Industry Applications, IEEE Transactions on , Volume: 39 , Issue: 4 , July-Aug.

[Abstract]   [PDF Full-Text (609 KB)]   **IEEE JNL**

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14 Model-based programming of intelligent embedded systems and robotic space explorers

[Abstract]   [PDF Full-Text (1139 KB)]   [Full-Text HTML]   **IEEE JNL**

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15 Operative diagnosis of graph-based systems with multiple faults

[Abstract]   [PDF Full-Text (204 KB)]   **IEEE JNL**

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1 **Using neural networks to automatically refine expert system knowledge bases: experiments in the NYNEX MAX domain**
Opitz, D.W.; Craven, M.W.; Shavlik, J.W.;
Neural Networks,1997., International Conference on , Volume: 1 , 9-12 June 1997
Pages:16 - 20 vol.1

[Abstract]   [PDF Full-Text (392 KB)]   **IEEE CNF**

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2 **System level diagnosis with local constraints**
Das, A.; Thulasiraman, K.; Lakshmanan, K.B.; Agarwal, V.K.;
Circuits and Systems, 1989., IEEE International Symposium on , 8-11 May 1989
Pages:1891 - 1894 vol.3

[Abstract]   [PDF Full-Text (332 KB)]   **IEEE CNF**

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3 **Detecting and diagnosing saturation faults**
Noriega, J.R.; Hong Wang;
Control '96, UKACC International Conference on (Conf. Publ. No. 427) , Volume: 2 , 2-5 Sept. 1996
Pages:809 - 813 vol.2

[Abstract]   [PDF Full-Text (384 KB)]   **IEE CNF**

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4 **Fault-locating and supervisory technique for multistaged branched optical networks**
Yung-Kuang Chen; Sien Chi;
Photonics Technology Letters, IEEE , Volume: 6 , Issue: 7 , July 1994
Pages:876 - 879

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5 **Conceptual design of a computer-driven display for monitoring reactor coolant mass**
Beltracchi, L.;
Nuclear Science, IEEE Transactions on , Volume: 38 , Issue: 3 , June 1991
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6 A knowledge-based system for the evaluation and redesign of digital circuit networks

Simoudis, E.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 8 , Issue: 3 , March 1989
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7 On the test and diagnosis of the perfect shuffle

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[Abstract]   [PDF Full-Text (671 KB)]   **IEEE CNF**

8 Detection and estimation of topology and parameter errors from real-time measurements

Mai Hoa Vuong; Lefebvre, S.; Xuan Dai Do;
Power Engineering Society Summer Meeting, 2002 IEEE , Volume: 3 , 21-25 July 2002
Pages:1565 - 1569 vol.3

[Abstract]   [PDF Full-Text (293 KB)]   **IEEE CNF**

9 Study of three phase induction motors with incipient rotor cage faults under different supply conditions

Nandi, S.; Bharadwaj, R.; Toliyat, H.A.; Parlos, A.G.;
Industry Applications Conference, 1999. Thirty-Fourth IAS Annual Meeting. Conference Record of the 1999 IEEE , Volume: 3 , 3-7 Oct. 1999
Pages:1922 - 1928 vol.3

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10 Local Console System using Java on personal computers for high energy accelerators

Kanaya, N.; Asaoka, S.; Maezawa, H.;
Particle Accelerator Conference, 1999. Proceedings of the 1999 , Volume: 2 , 27 March-2 April 1999
Pages:670 - 672 vol.2

[Abstract]   [PDF Full-Text (364 KB)]   **IEEE CNF**

11 Architecture and performance of the PEP-II low-level RF system

Corredoura, P.;
Particle Accelerator Conference, 1999. Proceedings of the 1999 , Volume: 1 , 27 March-2 April 1999
Pages:435 - 439 vol.1

[Abstract]   [PDF Full-Text (468 KB)]   **IEEE CNF**

12 Fault diagnosis of mixed signal VLSI systems using artificial neural networks

Nissar, A.I.; Upadhyaya, S.J.;
Mixed-Signal Design, 1999. SSMSD '99. 1999 Southwest Symposium on , 11-13 April 1999
Pages:93 - 98

[Abstract]   [PDF Full-Text (424 KB)]   **IEEE CNF**

13 DSTS: An expert system for diagnosis of advanced digital subscriber services

Eichen, E.; Brooks, D.; Burch, D.; Chippada, R.; Cousins, S.; Gang Fu; Lambert, G.; Rong, R.; Ruban, G.;

Network Operations and Management Symposium, 1998. NOMS 98., IEEE
, Volume. 3 , 15-20 Feb. 1998
Pages:795 - 804 vol.3

[Abstract]   [PDF Full-Text (644 KB)]   **IEEE CNF**

14 **Maintenance of asymmetrical digital subscriber lines (ADSLs)**
Gruber, J.G.;
Communications, 1994. ICC 94, SUPERCOMM/ICC '94, Conference Record,
Serving Humanity Through Communications. IEEE International Conference on
, 1-5 May 1994
Pages:437 - 443 vol.1

[Abstract]   [PDF Full-Text (560 KB)]   **IEEE CNF**

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1 Fault-Tolerant Software for Real-Time Applications   ■
H. Hecht
December 1976 **ACM Computing Surveys (CSUR)**, Volume 8 Issue 4

Full text available: 📄 pdf(1.43 MB) Additional Information: full citation, references, citings, index terms

2 Cellular Disco: resource management using virtual clusters on shared-memory ▬
multiprocessors
Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum
December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth ACM symposium on Operating systems principles**, Volume 33 Issue 5

Full text available: 📄 pdf(1.93 MB) Additional Information: full citation, abstract, references, citings, index terms

Despite the fact that large-scale shared-memory multiprocessors have been commercially available for several years, system software that fully utilizes all their features is still not available, mostly due to the complexity and cost of making the required changes to the operating system. A recently proposed approach, called Disco, substantially reduces this development cost by using a virtual machine monitor that leverages the existing operating system technology.In this paper we present a syste ...

3 The network architecture of the Connection Machine CM-5 (extended abstract) ▬
Charles E. Leiserson, Zahi S. Abuhamdeh, David C. Douglas, Carl R. Feynman, Mahesh N. Ganmukhi, Jeffrey V. Hill, Daniel Hillis, Bradley C. Kuszmaul, Margaret A. St. Pierre, David S. Wells, Monica C. Wong, Shaw-Wen Yang, Robert Zak
June 1992 **Proceedings of the fourth annual ACM symposium on Parallel algorithms and architectures**

Full text available: 📄 pdf(2.00 MB) Additional Information: full citation, references, citings, index terms

4 S-connect: from networks of workstations to supercomputer performance   ▬
Andreas G. Nowatzyk, Michael C. Browne, Edmund J. Kelly, Michael Parkin
May 1995 **ACM SIGARCH Computer Architecture News , Proceedings of the 22nd annual international symposium on Computer architecture**, Volume 23 Issue 2

Full text available: 📄 pdf(1.38 MB) Additional Information: full citation, abstract, references, citings, index terms

S-Connect is a new high speed, scalable interconnect system that has been developed to support networks of workstations to efficiently share computing resources. It uses off-the-shelf CMOS technology to direct drive fiber-optic systems at speeds greater than 1 Gbit/sec and can realize bisection bandwidths comparable to high-end MPP systems while being >10x more cost-effective. S-Connect systems do not rely on centralized switches, but rather are composed of adaptive, topology independen ...

5 Experience Using Multiprocessor Systems—A Status Report

Anita K. Jones, Peter Schwarz
June 1980 **ACM Computing Surveys (CSUR)**, Volume 12 Issue 2

Full text available: pdf(4.48 MB) Additional Information: full citation, references, citings, index terms

6 BFS—realization of a fault-tolerant architecture

Gerhard W. Geitz, Ernst J. Schmitter
May 1981 **Proceedings of the 8th annual symposium on Computer Architecture**

Full text available: pdf(483.41 KB) Additional Information: full citation, abstract, references, index terms

The paper considers possibilities of distributed architecture to improve the reliability of microcomputer systems to realize a fault-tolerant system. By using and extending existing redundancies of hardware, software, and time, a partially meshed ring structure that meets the requirements of a fault-tolerant architecture has been designed. Aspects of hardware implementation, system software structure, operating system requirements, fault diagnosis, and reconfiguration are explained, based o ...

7 Requirements and the concept of cooperative system management

Bharat Bhushan, Ahmed Patel
May 1998 **International Journal of Network Management**, Volume 8 Issue 3

Full text available: pdf(167.03 KB) Additional Information: full citation, abstract, references, citings, index terms

Cooperation among various types of management functions is necessary to allow management functions to interwork in providing and using information and services for systems management. To understand these tasks from the point of view of cooperative working, this article discusses the requirements and presents the concept of cooperative system management. © 1998 John Wiley & Sons, Ltd.

8 Papers: YESSIR: a simple reservation mechanism for the Internet

Ping Pan, Henning Schulzrinne
April 1999 **ACM SIGCOMM Computer Communication Review**, Volume 29 Issue 2

Full text available: pdf(1.23 MB) Additional Information: full citation, abstract, references, citings

RSVP has been designed to support resource reservation in the Internet. However, it has two major problems: complexity and scalability. The former results in large message processing overhead at end systems and routers, and inefficient firewall processing at the edge of the network. The latter implies that in a backbone environment, the amount of bandwidth consumed by refresh messages and the storage space that is needed to support a large number of flows at a router are too large. We have devel ...

9 Cellular disco: resource management using virtual clusters on shared-memory multiprocessors

Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum
August 2000 **ACM Transactions on Computer Systems (TOCS)**, Volume 18 Issue 3

Full text available: pdf(287.05 KB) Additional Information: full citation, abstract, references, citings, index terms, review

Despite the fact that large-scale shared-memory multiprocessors have been commercially available for several years, system software that fully utilizes all their features is still not available, mostly due to the complexity and cost of making the required changes to the operating system. A recently proposed approach, called Disco, substantially reduces this development cost by using a virtual machine monitor that laverages the existing operating system technology. In this paper we present a ...

10 Internet Nuggets

Mark Thorson
March 1996 **ACM SIGARCH Computer Architecture News,** Volume 24 Issue 1

Full text available: 📄 pdf(393.52 KB)   Additional Information: full citation, abstract

This column consists of selected traffic from the *comp.arch* newsgroup, a forum for discussion of computer architecture on Internet---an international computer network.

11 Knowledge based fault management for OSI networks

Celia A. Joseph, A. Sherzer, K. Muralidhar
June 1990 **Proceedings of the third international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1**

Full text available: 📄 pdf(826.21 KB)   Additional Information: full citation, abstract, references, index terms

The OSI Fault Management system (OSIFaM) is an evolving knowledge-based system for fault management of Open System Interconnection (OSI) networks. Our goal is to develop a knowledge-based tool that will reduce the expertise needed to recognize, diagnose and correct faults in OSI networks. For our first implementation, we are focusing on MAP 3.0 networks. This paper provides an overview of fault management in general, a brief survey of other fault management developments, the characteristics ...

12 Architecture of the IBM system/370

Richard P. Case, Andris Padegs
January 1978 **Communications of the ACM,** Volume 21 Issue 1

Full text available: 📄 pdf(2.78 MB)   Additional Information: full citation, abstract, references, citings, index terms

This paper discusses the design considerations for the architectural extensions that distinguish System/370 from System/360. It comments on some experiences with the original objectives for System/360 and on the efforts to achieve them, and it describes the reasons and objectives for extending the architecture. It covers virtual storage, program control, data-manipulation instructions, timing facilities, multiprocessing, debugging and monitoring, error handling, and input/output operations. ...

13 Selective interpretation as a technique for debugging computationally intensive programs

B. B. Chase, R. T. Hood
July 1987 **ACM SIGPLAN Notices , Papers of the Symposium on Interpreters and interpretive techniques,** Volume 22 Issue 7

Full text available: 📄 pdf(772.68 K)   Additional Information: full citation, abstract, index terms

As part of Rice University's project to build a programming environment for scientific software we have built a facility for program execution that solves some of the problems inherent in debugging large, computationally intensive programs. By their very nature such programs do not lend themselves to full-scale interpretation. In moderation however, interpretation can be extremely useful during the debugging process. In addition to discussing the particular benefits that we expect from interpre ...

14 Linux Print System at Cisco Systems, Inc.

Damian Ivereigh
October 1998 **Linux Journal**

Full text available: html(40.10 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>index terms</u>

Cisco runs a redundant system of 50 print servers using Linux, Samba and Netatalk. It prints to approximately 1,600 printers worldwide, serving 10,000 UNIX and Windows 95 users, some of whom are in mission-critical environments

15 Designing SoCs for yield improvement: Using embedded FPGAs for SoC yield improvement

Miron Abramovici, Charles Stroud, Marty Emmert
June 2002 **Proceedings of the 39th conference on Design automation**

Full text available: pdf(200.31 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

In this paper we show that an embedded FPGA core is an ideal host to implement infrastructure IP for yield improvement in a bus-based SoC. We present methods for testing, diagnosing, and repairing embedded FPGAs, for which complete testability is achieved without any area overhead or performance degradation. We show how an FPGA core can provide embedded testers for other cores in the SoC, so that cores designed to be tested with external vectors can be tested with BIST, and the entire SoC can be ...

16 Networks: A network-failure-tolerant message-passing system for terascale clusters

Richard L. Graham, Sung-Eun Choi, David J. Daniel, Nehal N. Desai, Ronald G. Minnich, Craig E. Rasmussen, L. Dean Risinger, Mitchel W. Sukalski
June 2002 **Proceedings of the 16th international conference on Supercomputing**

Full text available: pdf(148.66 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

The Los Alamos Message Passing Interface (LA-MPI) is an end-to-end network-failure-tolerant message-passing system designed for terascale clusters. LA-MPI is a standard-compliant implementation of MPI designed to tolerate network-related failures including I/O bus errors, network card errors, and wire-transmission errors. This paper details the distinguishing features of LA-MPI, including support for concurrent use of multiple types of network interface, and reliable message transmission utilizi ...

Keywords: MPI, fault tolerance, message passing

17 Improving the dependability of network management systems

Elias Procópio Duarte, Glenn Mansfield, Takashi Nanya, Shoichi Noguchi
July 1998 **International Journal of Network Management**, Volume 8 Issue 4

Full text available: pdf(147.51 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

As computer networks expand, there is a pressing need for management systems capable of handling errors. This article proposes an approach based on management proxies to improve the dependability of fault management systems. An effective MIB to implement the proxies is presented, which allows their

deployment at virtually no cost. As an example, a case study of a WAN is carried out. © 1998 John Wiley & Sons, Ltd.

18 SIGDA 2 - Design automation: Modular requirements for digital logic simulation at a predefined functional level

C. W. Hemming, S. A. Szygenda

August 1972 **Proceedings of the ACM annual conference - Volume 1**

Full text available: pdf(882.95 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>

Simulation of digital logic provides a viable technique for development and diagnosis of digital systems. Simulation models currently employed are discussed with a summary of structure and timing techniques. A methodology for functional simulation in conjunction with gate level simulation is discussed, presenting a representative set of predefined functions, and introducing a measure for predefined function performance. Errors in design detectable at the functional level are catagorized.

Keywords: diagnosis of digital systems, digital simulation, fault simulation, functional simulation, logic design

19 Network performance reporting

K. Terplan

April 1982 **Proceedings of the Computer Network Performance Symposium**

Full text available: pdf(655.20 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Managing networks using Network Administration Centers is increasingly considered. After introducing the information demand for operational, tactical and strategic network management the paper is dealing with the investigation of the applicability of tools and techniques for these areas. Network monitors and software problem determination tools are investigated in greater detail. Also implementation details for a multihost-multinode network including software and hardware tools combined by ...

20 Slipstream processors: improving both performance and fault tolerance

Karthik Sundaramoorthy, Zach Purser, Eric Rotenburg

November 2000 **Proceedings of the ninth international conference on Architectural support for programming languages and operating systems**, Volume 28 , 34 Issue 5 , 5

Full text available: pdf(111.54 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

Processors execute the full dynamic instruction stream to arrive at the final output of a program, yet there exist shorter instruction streams that produce the same overall effect. We propose creating a shorter but otherwise equivalent version of the original program by removing ineffectual computation and computation related to highly-predictable control flow. The shortened program is run concurrently with the full program on a chip multiprocessor simultaneous multithreaded processor, with two ...

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1 Cellular Disco: resource management using virtual clusters on shared-memory multiprocessors ▰
Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum
December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth ACM symposium on Operating systems principles,** Volume 33 Issue 5

Full text available: 📄 pdf(1.93 MB)   Additional Information: full citation, abstract, references, citings, index terms

Despite the fact that large-scale shared-memory multiprocessors have been commercially available for several years, system software that fully utilizes all their features is still not available, mostly due to the complexity and cost of making the required changes to the operating system. A recently proposed approach, called Disco, substantially reduces this development cost by using a virtual machine monitor that leverages the existing operating system technology.In this paper we present a syste ...

2 Experience Using Multiprocessor Systems—A Status Report ▰
Anita K. Jones, Peter Schwarz
June 1980 **ACM Computing Surveys (CSUR),** Volume 12 Issue 2

Full text available: 📄 pdf(4.48 MB) Additional Information: full citation, references, citings, index terms

3 Internet Nuggets ▰
Mark Thorson
March 1996 **ACM SIGARCH Computer Architecture News,** Volume 24 Issue 1

Full text available: 📄 pdf(393.52 KB)   Additional Information: full citation, abstract

This column consists of selected traffic from the *comp.arch* newsgroup, a forum for discussion of computer architecture on Internet---an international computer network.

4 Cellular disco: resource management using virtual clusters on shared-memory multiprocessors ▰
Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum
August 2000 **ACM Transactions on Computer Systems (TOCS),** Volume 18 Issue 3

Full text available: 📄 pdf(287.05 KB)   Additional Information: full citation, abstract, references, citings, index terms, review

Despite the fact that large-scale shared-memory multiprocessors have been commercially avai●ble for several years, system software●at fully utilizes all their features is still n●●vailable, mostly due to the complexit●●d cost of making the required changes to the operating system. A recently proposed approach, called Disco, substantially reduces this development cost by using a virtual machine monitor that laverages the existing operating system technology. In this paper we present a ...

Keywords: fault containment, resource managment, scalable multiprocessors, virtual machines

5 Architecture of the IBM system/370

Richard P. Case, Andris Padegs
January 1978 **Communications of the ACM,** Volume 21 Issue 1

Full text available: 🗎 pdf(2.78 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

This paper discusses the design considerations for the architectural extensions that distinguish System/370 from System/360. It comments on some experiences with the original objectives for System/360 and on the efforts to achieve them, and it describes the reasons and objectives for extending the architecture. It covers virtual storage, program control, data-manipulation instructions, timing facilities, multiprocessing, debugging and monitoring, error handling, and input/output operations. ...

Keywords: architecture, computer systems, error handling, instruction sets, virtual storage

6 Knowledge based fault management for OSI networks

Celia A. Joseph, A. Sherzer, K. Muralidhar
June 1990 **Proceedings of the third international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1**

Full text available: 🗎 pdf(826.21 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

The OSI Fault Management system (OSIFaM) is an evolving knowledge-based system for fault management of Open System Interconnection (OSI) networks. Our goal is to develop a knowledge-based tool that will reduce the expertise needed to recognize, diagnose and correct faults in OSI networks. For our first implementation, we are focusing on MAP 3.0 networks. This paper provides an overview of fault management in general, a brief survey of other fault management developments, the characteristics ...

7 Selective interpretation as a technique for debugging computationally intensive programs

B. B. Chase, R. T. Hood
July 1987 **ACM SIGPLAN Notices , Papers of the Symposium on Interpreters and interpretive techniques,** Volume 22 Issue 7

Full text available: 🗎 pdf(772.68 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>index terms</u>

As part of Rice University's project to build a programming environment for scientific software, we have built a facility for program execution that solves some of the problems inherent in debugging large, computationally intensive programs. By their very nature such programs do not lend themselves to full-scale interpretation. In moderation however, interpretation can be extremely useful during the debugging process. In addition to discussing the particular benefits that we expect from interpre ...

8 <u>Slipstream processors: improving both performance and fault tolerance</u>

Karthik Sundaramoo●y, Zach Purser, Eric Rotenburg

November 2000 **Pro●dings of the ninth international c●erence on Architectural support for programming languages and operating systems**, Volume 28 , 34 Issue 5 , 5

Full text available: <u>pdf(111.54 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

Processors execute the full dynamic instruction stream to arrive at the final output of a program, yet there exist shorter instruction streams that produce the same overall effect. We propose creating a shorter but otherwise equivalent version of the original program by removing ineffectual computation and computation related to highly-predictable control flow. The shortened program is run concurrently with the full program on a chip multiprocessor simultaneous multithreaded processor, with two ...

9 <u>Slipstream processors: improving both performance and fault tolerance</u>

Karthik Sundaramoorthy, Zach Purser, Eric Rotenberg

November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

Full text available: <u>pdf(1.51 MB)</u> Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Processors execute the full dynamic instruction stream to arrive at the final output of a program, yet there exist shorter instruction streams that produce the same overall effect. We propose creating a shorter but otherwise equivalent version of the original program by removing ineffectual computation and computation related to highly-predictable control flow. The shortened program is run concurrently with the full program on a chip multiprocessor or simultaneous multithreaded processor, with t ...

10 <u>Network performance reporting</u>

K. Terplan

April 1982 **Proceedings of the Computer Network Performance Symposium**

Full text available: <u>pdf(655.20 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Managing networks using Network Administration Centers is increasingly considered. After introducing the information demand for operational, tactical and strategic network management the paper is dealing with the investigation of the applicability of tools and techniques for these areas. Network monitors and software problem determination tools are investigated in greater detail. Also implementation details for a multihost-multinode network including software and hardware tools combined by ...

11 <u>BPF+: exploiting global data-flow optimization in a generalized packet filter architecture</u>

Andrew Begel, Steven McCanne, Susan L. Graham

August 1999 **ACM SIGCOMM Computer Communication Review , Proceedings of the conference on Applications, technologies, architectures, and protocols for computer communication**, Volume 29 Issue 4

Full text available: <u>pdf(1.55 MB)</u> Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

A *packet filter* is a programmable selection criterion for classifying or selecting packets from a packet stream in a generic, reusable fashion. Previous work on packet filters falls roughly into two categories, namely those efforts that investigate flexible and extensible filter abstractions but sacrifice performance, and those that focus on low-level, optimized filtering representations but sacrifice flexibility. Applications like network monitoring and intrusion detection, however, requ ...

12 <u>Microcode implemented General Modular Redundancy</u>

F. P. Mathur, P. T. de Sousa

September 1974 **Conference record of the 7th annual workshop on Microprogramming**

Full text available: pdf(9.37 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

First the concepts of protective redundancy are described in the unified framework called General Modular Redundancy (GMR). GMR is a unified framework which synthesizes all the major redundancy techniques known. An alternative to an exclusively hardware implementation is by means of an extension to the Wensleyian Software Implemented Fault-Tolerance (SIFT) approach. A more attractive alternative, an implementation in microcode, is proposed and described here.

13 <u>The <bigwig> project</u>

Claus Brabrand, Anders Møller, Michael I. Schwartzbach
May 2002 **ACM Transactions on Internet Technology (TOIT)**, Volume 2 Issue 2

Full text available: pdf(586.33 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

We present the results of the <bigwig> project, which aims to design and implement a high-level domain-specific language for programming interactive Web services.

A fundamental aspect of the development of the World Wide Web during the last decade is the gradual change from static to dynamic generation of Web pages. Generating Web pages dynamically in dialog with the client has the advantage of providing up-to-date and tailor-made information. The development of systems ...

Keywords: Interactive Web services, program analysis

14 <u>Architectural considerations for a microprogrammable emulating engine using bit-slices</u>

C. Halatsis, A. van Dam, J. Joosten, M. Letheren
May 1980 **Proceedings of the 7th annual symposium on Computer Architecture**

Full text available: pdf(951.32 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

This paper describes architectural considerations which led to the design of a fast programmable processor made from ECL bit-slices. The processor will be used as an on-line data filtering engine for high energy physics experiments. Unlike prior designs of such engines, the processor supports both user (horizontal) microcode and emulation of the PDP-11 fixed point instruction set (without memory management and multiple interrupt levels). In addition to an overview of the techniques used to ...

15 <u>Session 4: innovative solutions: Survival by defense-enabling</u>

Partha Pal, Franklin Webber, Richard Schantz
September 2001 **Proceedings of the 2001 workshop on New security paradigms**

Full text available: pdf(783.75 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Attack survival, which means the ability to provide some level of service despite an ongoing attack by tolerating its impact, is an important objective of security research. In this paper we present a new approach to survivability and intrusion tolerance. Our approach, which we call "survival by defense" is based on the observation that many applications can be given increased resistance to malicious attack even though the environment in which they run is untrustworthy. This paper describes the ...

16 A Functional Description of ANALYZE: A Computer-Assisted Analysis System
for Linear Programming Models

Harvey Greenberg

March 1983 **ACM Transactions on Mathematical Software (TOMS),** Volume 9
Issue 1

Full text available: pdf(2.49 MB) Additional Information: full citation, references, citings, index terms

17 Representing knowledge: part III. Frames: Minsky's frame system theory

June 1975 **Proceedings of the 1975 workshop on Theoretical issues in natural
language processing**

Full text available: pdf(1.14 MB)
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